

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): S. Brandon Keller, et al.

Confirmation No.: 2411

Application No.: 10/647,597

Examiner: Magid Y. Dimyan

Filing Date: Aug. 25, 2003

Group Art Unit: 2825

Title: System And Method For Determining Wire Capacitance For A VLSI Circuit

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir:

Transmitted herewith is/are the following in the above-identified application:

- (X) Response/Amendment () Petition to extend time to respond
() New fee as calculated below () Supplemental Declaration
(X) No additional fee
(X) Other: IDS Tran, Form 1449, Stmt. 1.97e), and Post Card (fee \$)

CLAIMS AS AMENDED BY OTHER THAN A SMALL ENTITY						
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR	(5) PRESENT EXTRA	(6) RATE	(7) ADDITIONAL FEES
TOTAL CLAIMS	21	MINUS	21	= 0	X \$50	\$ 0
INDEP. CLAIMS	6	MINUS	6	= 0	X \$200	\$ 0
[] FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM					+ \$360	\$ 0
EXTENSION FEE	1ST MONTH \$120.00	2ND MONTH \$450.00	3RD MONTH \$1020.00	4TH MONTH \$1590.00		\$ 0
OTHER FEES						\$
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT						\$ 0

Charge \$ 0 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

"Express Mail" label no. EV413219541US

Date of Deposit Dec. 1, 2005

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By

Typed Name: Melissa Smith

Respectfully submitted,

S. Brandon Keller, et al.

By

Curtis A. Vock

Attorney/Agent for Applicant(s)

Reg. No. 38,356

Date: Dec. 1, 2005

Telephone No.: (720) 931-3011



PATENT
Attorney Docket No.: 100111221-1
Express Mail Label No.: EV413219541US

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Applicant(s):	S. Brandon Keller, et al.	Confirmation No.	2411
Serial No.	10/647,597	Group Art Unit:	2825
Filed:	August 25, 2003	Examiner:	Dimyan, Magid Y
For:	System And Method For Determining Wire Capacitance for a VLSI Circuit	Docket No.	100111221-1

December 1, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE

Sir:

In response to the Office Action mailed September 14, 2005, our amendments and response are as follows:

SPECIFICATION begins on page 2 of this paper.

CLAIMS begin on page 4 of this paper.

REMARKS begins on page 12 of this paper.